# **Power Factor Correction**

Name

# Introduction

Voltage and current waveforms as applied to complex loads tell us a great deal about the power environment surrounding these loads. This lab will guide you through the analysis of a simple circuit with a sinusoidal excitation. You will make measurements to calculate various power quantities, and learn how to improve the power factor.

# **Objectives**

- Apply classroom principles and theory to physical devices;
- Understand complex power issues using a real circuit;
- Understand the benefits of power factor correction.

# Equipment

- Waveforms software;
- Digilent Analog Discovery 2 Module;
- Breadboard;
- Assorted components and wires.

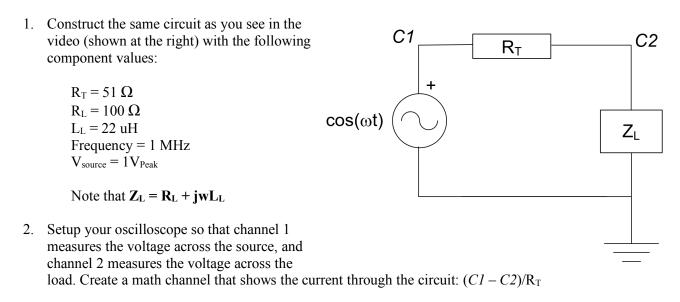
#### References

- Digilent Real Audio YouTube video;
- Zybooks text book;
- Course web site;
- Resistor color-code chart.

#### Procedure

Your lab this week will be shorter, providing you more time to focus on getting your final assignments done and studying for the final quiz. You will be duplicating the work done in the video whose link is below. The video and this lab exercise will help you appreciate the issues involved in power generation, transmission, and usage.

#### https://www.youtube.com/watch?v=k0mdwQvn2iw&list=PL170A01159D42313D&index=25



3. Calculate the current through and the average power dissipated in  $R_T$ 

I =

P = \_\_\_\_\_

4. Calculate the power factor of the load.

5. Calculate and measure the phase difference between the voltage across and the current through the load.

(θ<sub>V</sub>-θ<sub>I</sub>)<sub>calc</sub> \_\_\_\_\_ (θ<sub>V</sub>-θ<sub>I</sub>)<sub>meas</sub> \_\_\_\_\_

- 6. Place your 0.1 $\mu$ F capacitor across the load. Make sure it is in parallel with both R<sub>L</sub> and the inductor.
- 7. Calculate the power factor of the load with the capacitor added in parallel.

pf \_\_\_\_\_

8. Calculate the current through and the average power dissipated in  $R_T$  with the power factor correction capacitor added across the load.

I = \_\_\_\_\_

9. Oops – things didn't go quite as planned, did they? Explain below what you found out, and how you might correct this situation (i.e. how to bring the PF of the load to unity thereby reducing the amount of current required from the source).

10. Submit your finished lab to the lab8 drop box by 4pm on Friday, June 5.