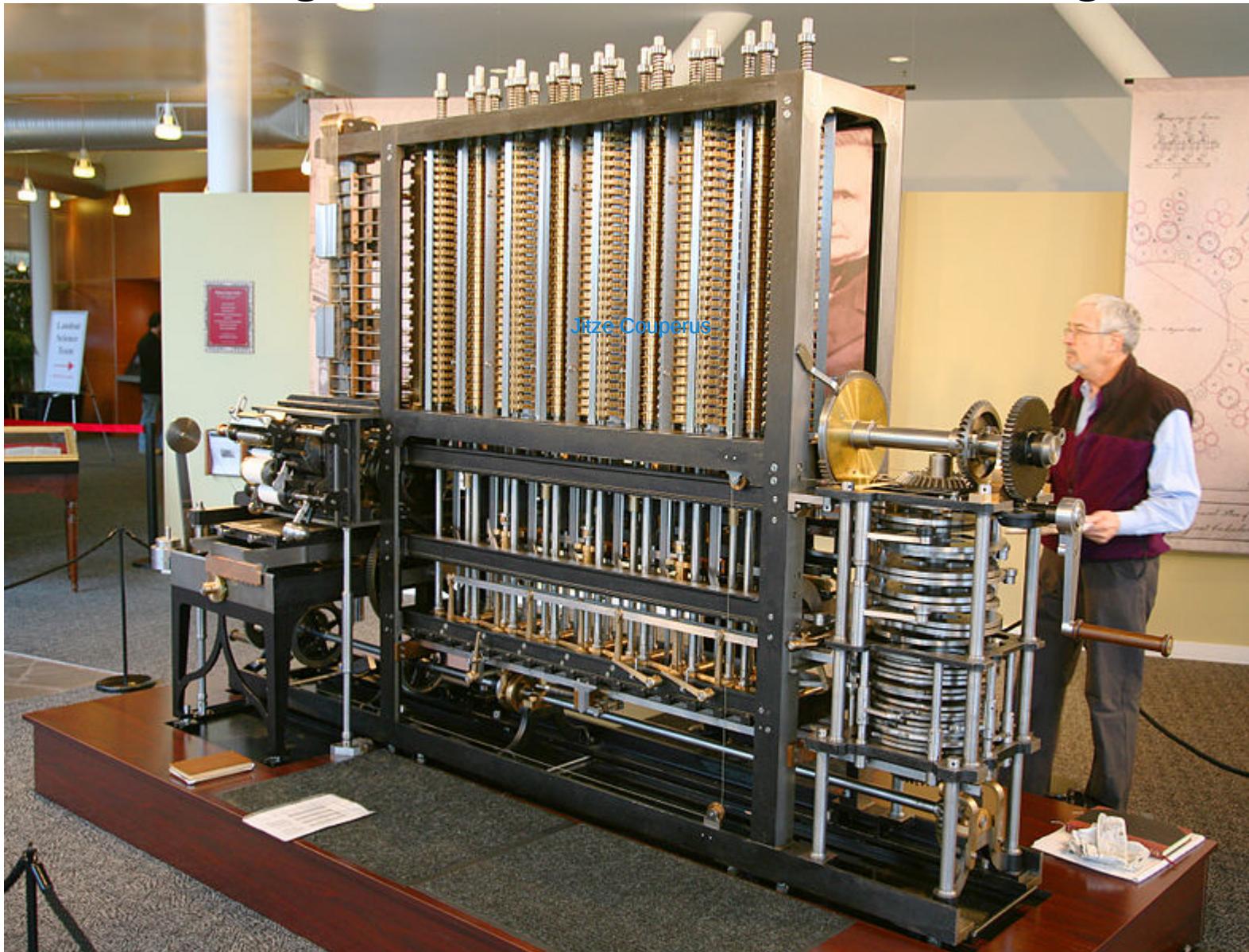
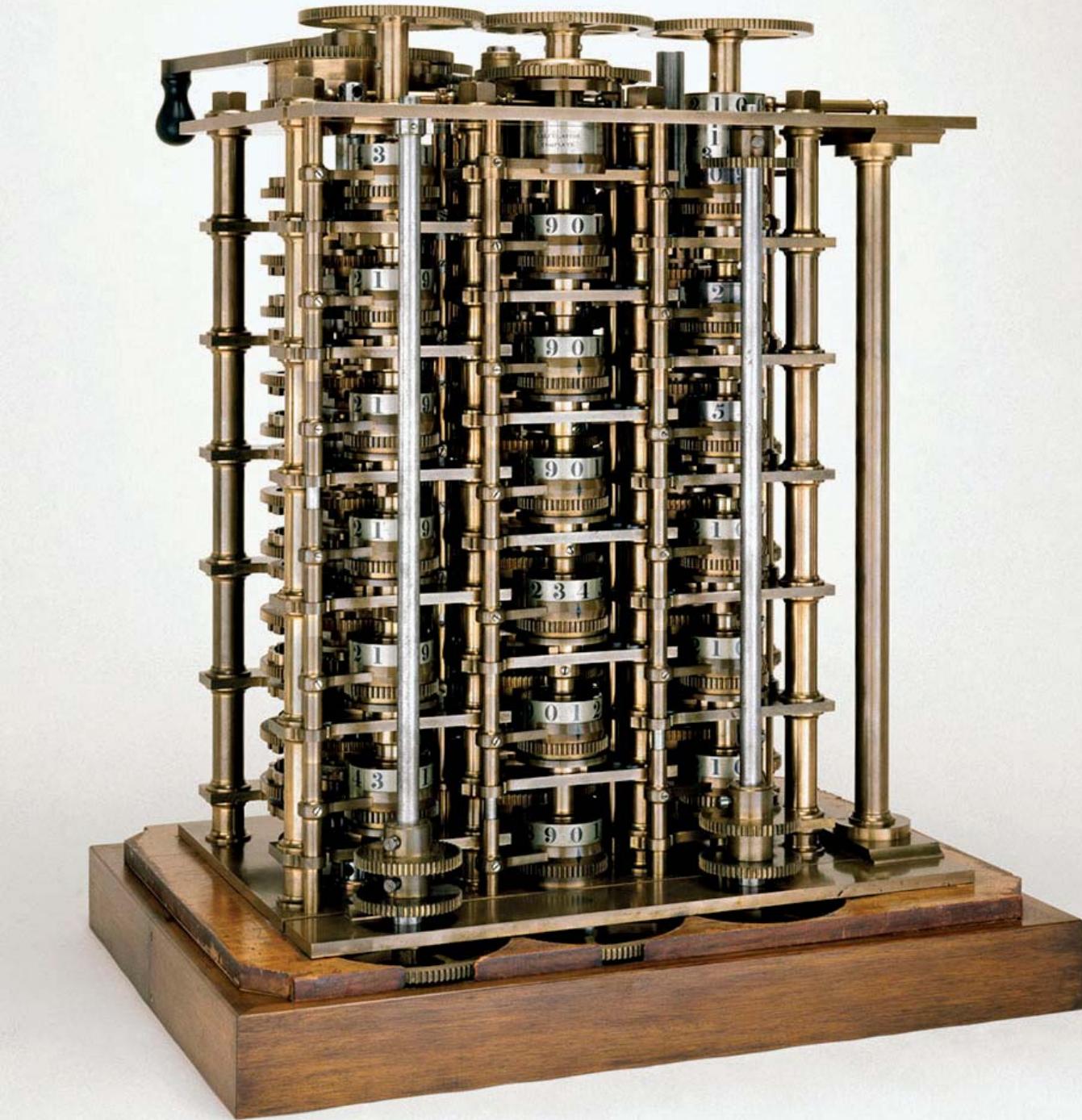


ENGR-354 Digital Logic

Intro to logic circuits and boolean algebra



Jitze Couperus

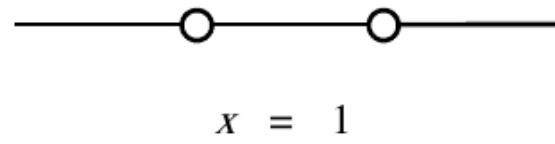
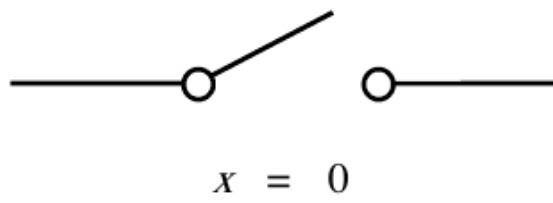


Science Museum
London

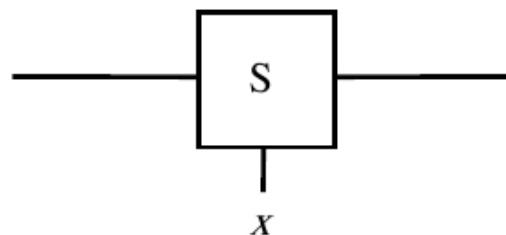
Binary Logic Circuits

- Logic circuits perform operations on digital signals;
- These circuits are implemented using electronic components;
- Binary logic circuits can be found in one of two states
 - 0 or 1;
 - off or on;
 - down or up;
 - not asserted or asserted;
 - etc.

Switch Representation



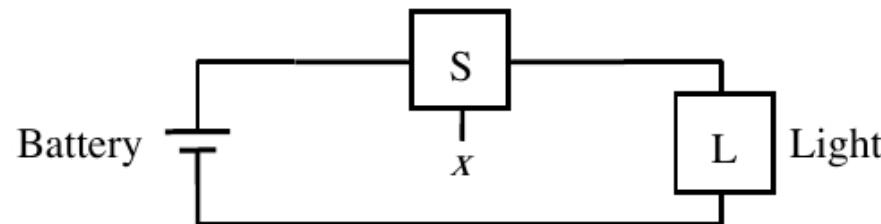
(a) Two states of a switch



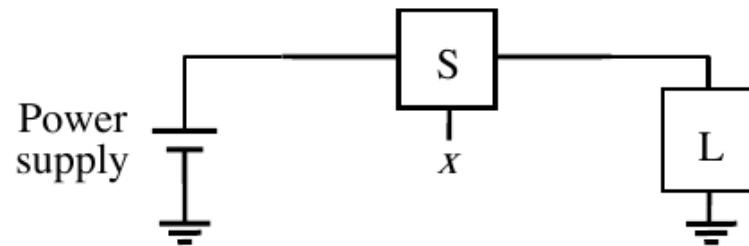
(b) Symbol for a switch

Switch Example

$$L(x) = x$$

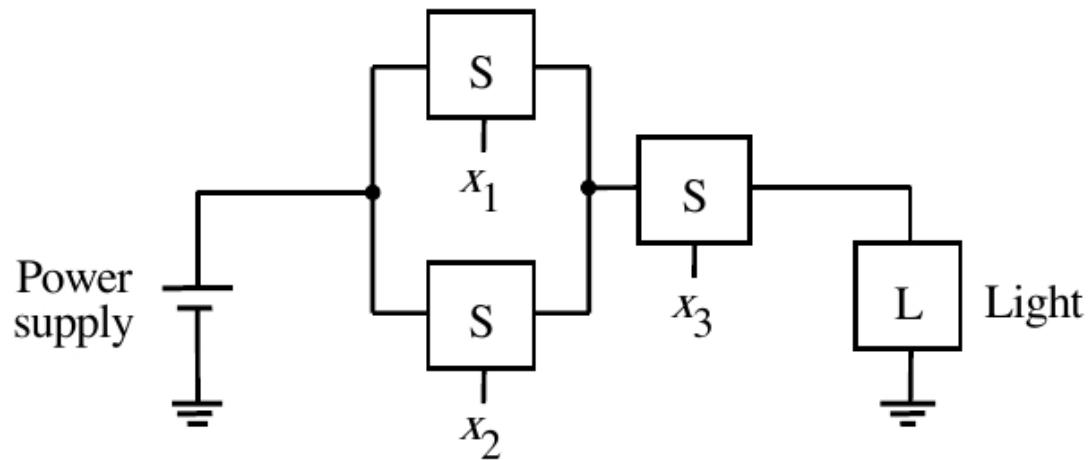


(a) Simple connection to a battery



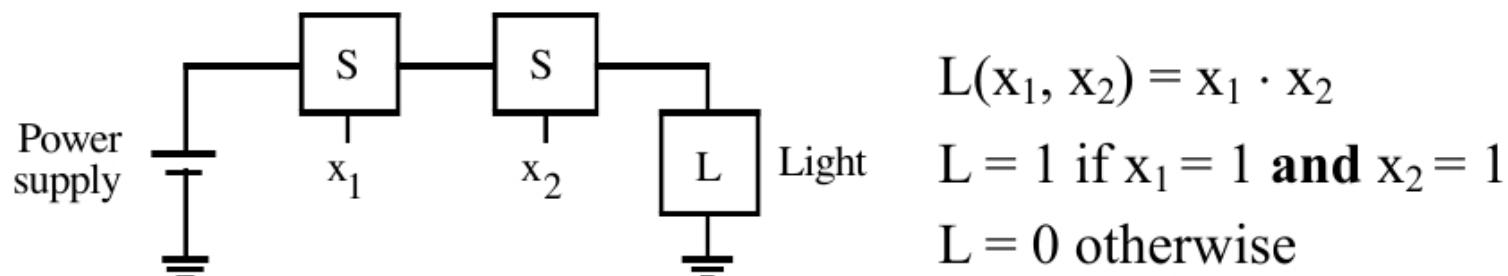
(b) Using a ground connection as the return path

A Series-Parallel Example

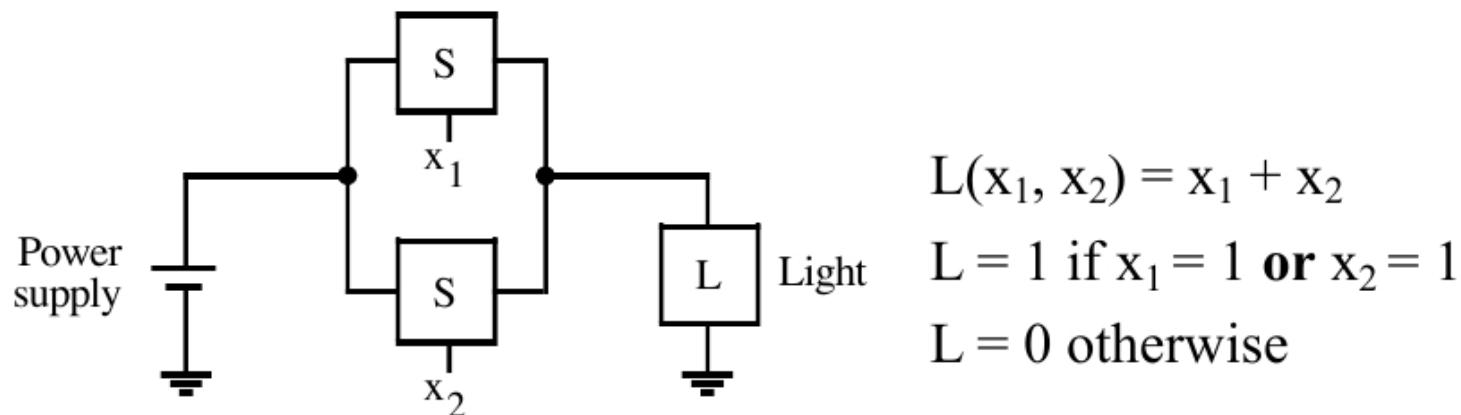


$$L(x_1, x_2, x_3) = (x_1 + x_2) \cdot x_3$$

Two Basic Functions

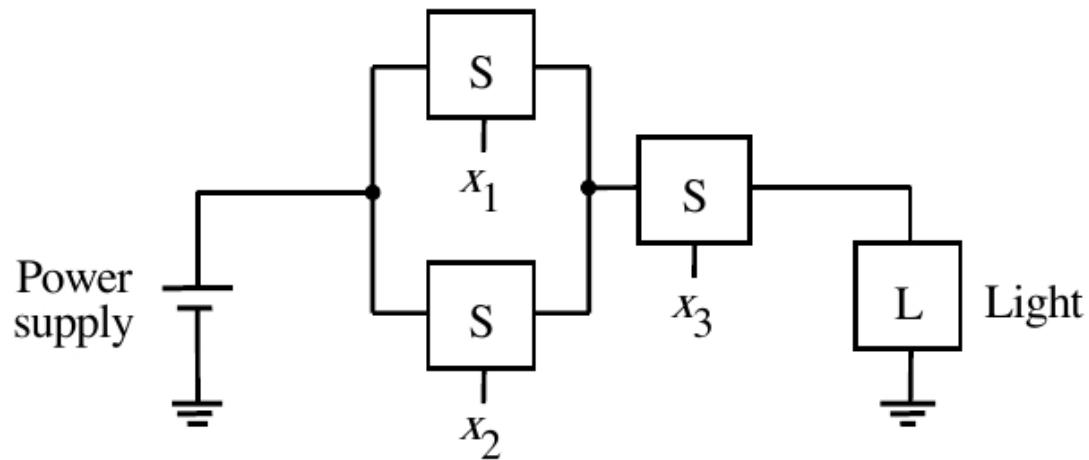


(a) The logical AND function (series connection)



(b) The logical OR function (parallel connection)

A Series-Parallel Example



$$L(x_1, x_2, x_3) = (x_1 + x_2) \cdot x_3$$

Truth Tables

- All combinations of inputs on the left;
- Outputs on the right;
- 2-input **AND** and **OR** functions shown below.

x_1	x_2	$x_1 \cdot x_2$	$x_1 + x_2$
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	1

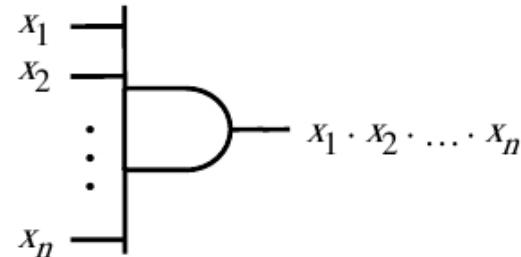
AND

OR

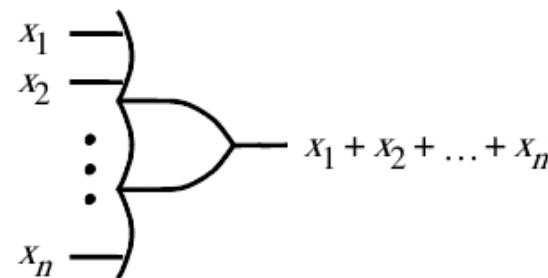
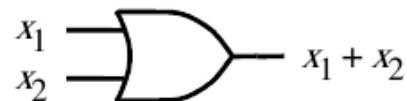
3-Input And and Or Functions

x_1	x_2	x_3	$x_1 \cdot x_2 \cdot x_3$	$x_1 + x_2 + x_3$
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	0	1
1	0	0	0	1
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

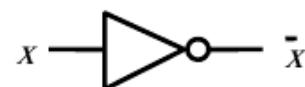
Basic Gates



(a) AND gates

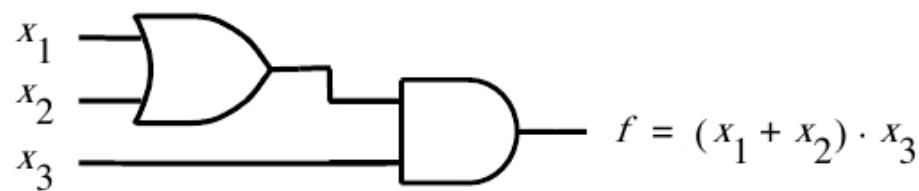


(b) OR gates

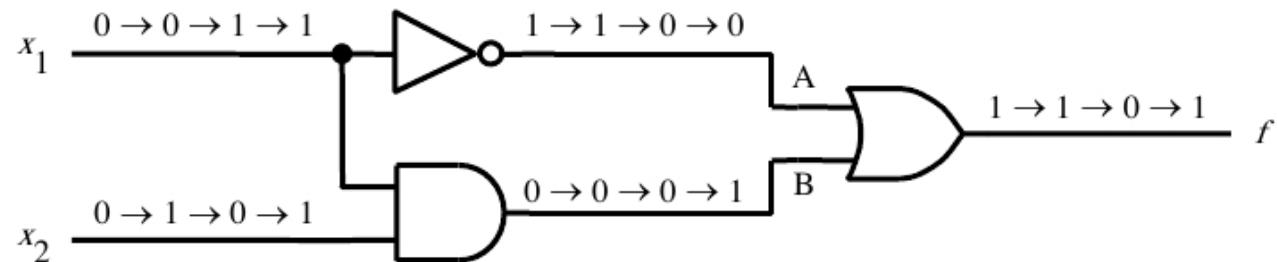


(c) NOT gate

Example Using Basic Gates



Sequencing of Inputs

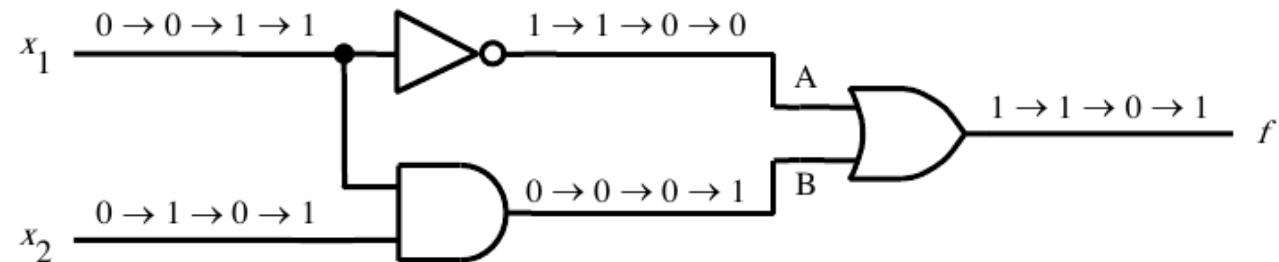


Network that implements $f = \overline{x}_1 + x_1 \cdot x_2$

x_1	x_2	$f(x_1, x_2)$
0	0	1
0	1	1
1	0	0
1	1	1

Truth table for f

Sequencing of Inputs

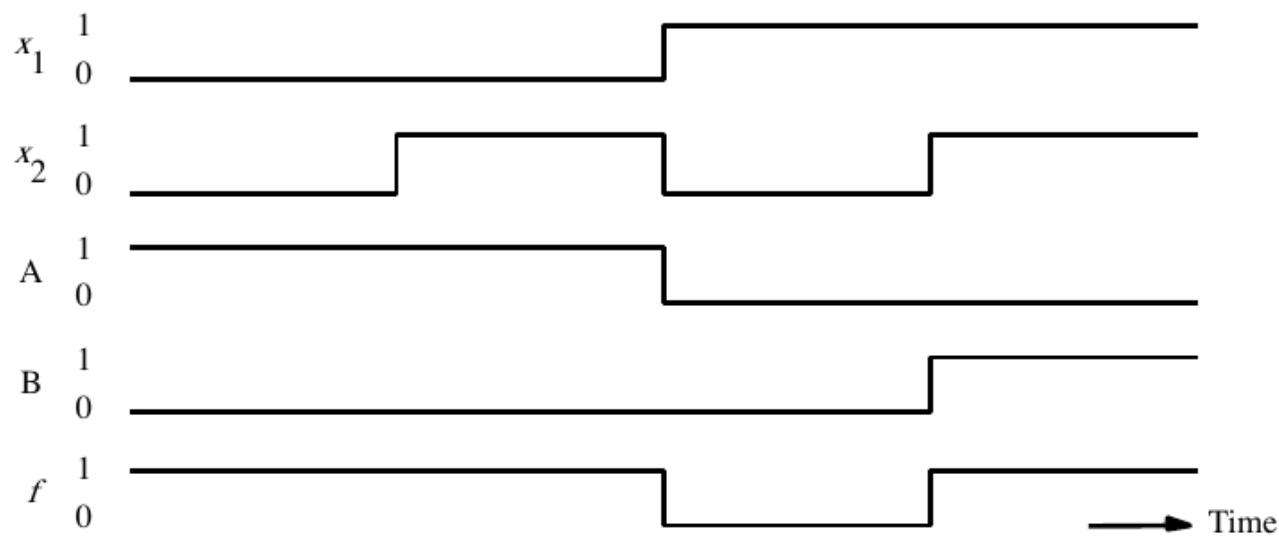
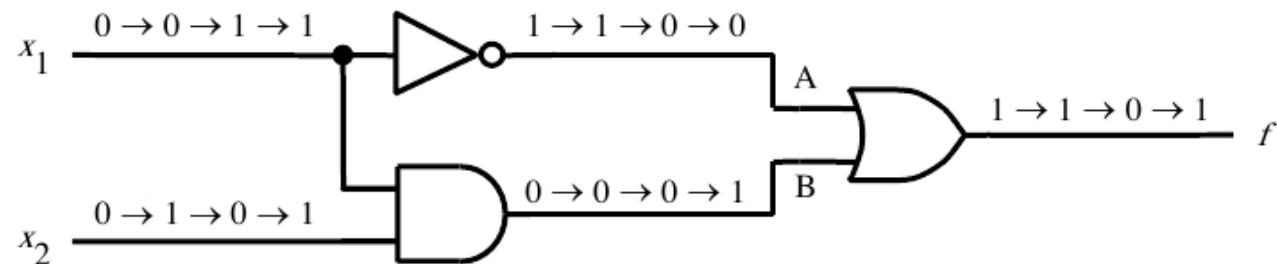


Network that implements $f = \overline{x}_1 + x_1 \cdot x_2$

x_1	x_2	$f(x_1, x_2)$
0	0	1
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1	1	1

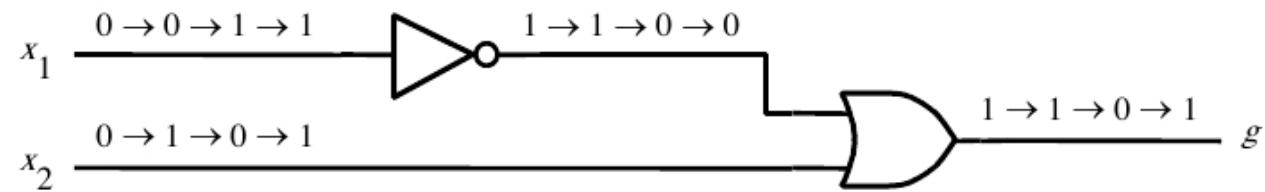
Truth table for f

Timing Diagram



Timing diagram

Example



Network that implements $g = \overline{x}_1 + x_2$

- Draw a timing diagram below:

Boolean Algebra

- In 1849, George Boole published a scheme for describing logical thought and reasoning;
- In 1930s, Claude Shannon applied Boolean algebra to describe circuits built with switches;
- Boolean algebra provides the mathematical foundation for digital design.

Notation

- INVERSION: $\bar{x} = x' = !x = \text{NOT } x$
 $f(\bar{x}_1, \bar{x}_2) = \overline{x_1 + x_2} = (\bar{x}_1 + \bar{x}_2)' = !(x_1 + x_2)$
 $= \text{NOT}(x_1 + x_2)$
- AND: $x_1 \cdot x_2 = x_1 \wedge x_2 = x_1 \ x_2$
- OR: $x_1 + x_2 = x_1 \vee x_2$

Precedence of Operations

- In the absence of parentheses, operations are performed in this order: NOT, AND, OR

$$x_1 x_2 + x_1' x_2' = (x_1 x_2) + ((x_1') (x_2'))$$

Principle of Duality

- On the following pages, axioms and theorems are listed in pairs to show the principle of duality;
- Given a logic expression, its *dual* is found by exchanging + and • operators and 0's and 1's;
- The dual of any true statement is **always** true.

Axioms of Boolean Algebra

$$1. \quad 0 \cdot 0 = 0$$

$$1 + 1 = 1$$

$$2. \quad 1 \cdot 1 = 1$$

$$0 + 0 = 0$$

$$3. \quad 0 \cdot 1 = 1 \cdot 0 = 0$$

$$1 + 0 = 0 + 1 = 1$$

$$4. \quad \text{if } x = 0 \text{ then } \bar{x} = 1$$

$$\text{if } x = 1 \text{ then } \bar{x} = 0$$

Single-Variable Theorems

$$5. \quad x \cdot 0 = 0$$

$$x + 1 = 1$$

$$6. \quad x \cdot 1 = x$$

$$x + 0 = x$$

$$7. \quad x \cdot x = x$$

$$x + x = x$$

$$8. \quad x \cdot \overline{x} = 0$$

$$x + \overline{x} = 1$$

$$9. \quad \overline{\overline{x}} = x$$

2- and 3-Variable Properties

- | | |
|--|--------------|
| 10a. $x \cdot y = y \cdot x$ | Commutative |
| 10b. $x + y = y + x$ | |
| 11a. $x \cdot (y \cdot z) = (x \cdot y) \cdot z$ | Associative |
| 11b. $x + (y + z) = (x + y) + z$ | |
| 12a. $x \cdot (y + z) = x \cdot y + x \cdot z$ | Distributive |
| 12b. $x + y \cdot z = (x + y) \cdot (x + z)$ | |

2- and 3-Variable Properties

$$13a. x + x \cdot y = x \quad \text{Absorption}$$

$$13b. x \cdot (x + y) = x$$

$$14a. x \cdot y + x \cdot \bar{y} = x \quad \text{Combining}$$

$$14b. (x + y) \cdot (x + \bar{y}) = x$$

$$15a. \overline{x \cdot y} = \overline{x} + \overline{y} \quad \text{DeMorgan's Thm}$$

$$15b. \overline{x + y} = \overline{x} \cdot \overline{y}$$

$$16. x + \bar{x} \cdot y = x + y \quad x \cdot (\bar{x} + y) = x \cdot y$$

Truth Table Proof of DeMorgan's Theorem

15a. $\overline{x \cdot y} = \overline{x} + \overline{y}$ DeMorgan's Theorem

x	y	$x \cdot y$	$\overline{x \cdot y}$	\overline{x}	\overline{y}	$\overline{x} + \overline{y}$
0	0	0	1	1	1	1
0	1	0	1	1	0	1
1	0	0	1	0	1	1
1	1	1	0	0	0	0

$\underbrace{\hspace{10em}}$ LHS $\underbrace{\hspace{10em}}$ RHS

BOOLEAN ALGEBRA EXAMPLES

$$1) (A + B)(\bar{A} + \bar{B}) =$$

$$2) \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + A\bar{B}\bar{C} + A\bar{B}C =$$

